

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Zhidan L. TOLT	Docket No:	372668-00400 (362842)
Serial No.:	10/707,342	Confirmation No.:	1341
Filed:	December 5, 2003	Group Art Unit:	2815
For:	LOW VOLTAGE ELECTRON SOURCE WITH SELF ALIGNED GATE APERTURES, FABRICATION METHOD THEREOF, AND LUMINOUS DISPLAY USING THE ELECTRON SOURCE	Examiner:	Paul A. Budd

**REQUEST FOR CONTINUED EXAMINATION (RCE)
UNDER 37 CFR § 1.114**

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

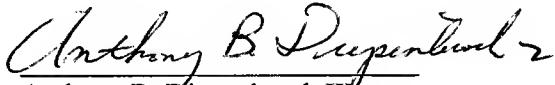
A Request for Continued Examination (RCE) of the above-referenced application is hereby submitted.

REMARKS

I. INFORMATION DISCLOSURE STATEMENT

The Examiner's attention is hereby directed to the submission of an Information Disclosure Statement (IDS) respecting this application, in accordance with 37 CFR § 1.56. A copy the IDS submission is attached herein, along with form PTO-1449. Consideration of the references is hereby requested.

Respectfully submitted,


Anthony B. Diepenbrock III
Anthony B. Diepenbrock III
Reg. No. 39,960

Dated: October 30, 2007

DECHERT LLP
Customer No. 37509
Tel: (650) 813-4800
Fax: (650) 813-4848